

# PATENT ABSTRACTS OF JAPAN

(11)Publication number :

2002-057417

(43)Date of publication of application : 22.02.2002

(51)Int.Cl.

H05K 1/02  
G01R 31/319  
G01R 31/3183  
G01R 31/28  
H05K 3/00  
H05K 3/46

(21)Application number : 2000-241094

(71)Applicant : FUJITSU TEN LTD

(22)Date of filing : 09.08.2000

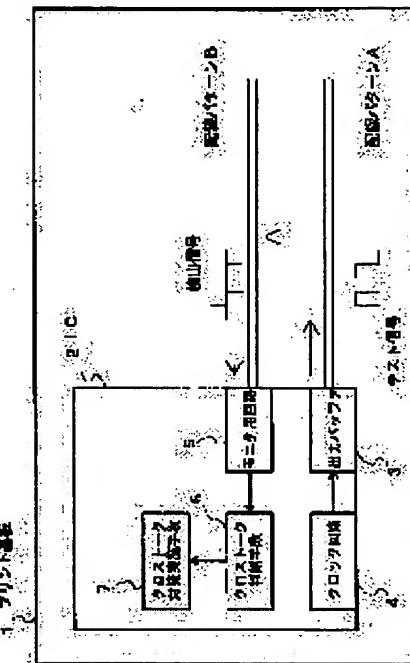
(72)Inventor : KOMATSU KAZUHIRO  
KIDO KEISUKE

## (54) PRINTED BOARD, AND INTEGRATED CIRCUIT, AND ELECTRONIC COMPONENT

### (57)Abstract:

**PROBLEM TO BE SOLVED:** To provide a printed board in which generation of crosstalk can be blocked, even if miniaturization of electronic component advances and a significantly narrow interval is set between wiring patterns.

**SOLUTION:** In an electronic component comprising an integrated circuit mounted on a printed board, a wiring pattern A fed with a test signal for detecting crosstalk between the conduction patterns of the printed board and a wiring pattern B for receiving a radiation wave from the wiring pattern A, in response to the test signal are formed on the printed board. The integrated circuit comprises a test terminal connected with the wiring pattern A and outputting the test signal, a test terminal connected with the wiring pattern B, and a means for determining the intensity of crosstalks from a detection signal inputted to the test terminal.



### LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's  
decision of rejection]

[Date of extinction of right]

Copyright (C) 1998,2003 Japan Patent Office